



Authors:

Tram Chase SimVentions, Inc. tramchase@simventions.com

Traci McDonald SimVentions, Inc. tracimcdonald@simventions.com Matt Wilson SimVentions, Inc. mattwilson@simventions.com

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1 INTRODUCTION

Aerospace and avionics systems have strict requirements for reliability and redundancy. However, like all systems development efforts, there are limited budgets and time constraints and a need to deliver increased capability on shorter timelines. One method for addressing this problem is establishing and using open technical architectures and solutions that leverage Digital Engineering (DE) innovations and opportunities.

Many open architecture efforts to enable reuse, extensibility, integration, and open interfaces in the avionics domain have been focused on the software aspect of the avionics system. The desire for open systems and reuse coupled with the difficulty of achieving and maintaining interoperability are just as prevalent in the computing hardware domain as in the software domain.

Simple design questions are very real and currently very difficult to address, such as: What computing chassis or backplanes are available? Which boards fit into which chassis? What form factors are available? Are the electrical and thermal loads appropriate and compatible with the operating environment? Can I replace a board from Vendor A with a board from Vendor B?

Even before addressing those kinds of questions, it is necessary to explore issues like: Can we find boards from multiple vendors that have comparable capabilities and operating requirements? Can smaller form factor boards be used without affecting the mission?



Figure 1-1: Introduction - HHITS tradeoffs for component integration.¹

¹ To help address these issues, the US Navy helped to establish the Hardware Open Systems Technologies (HOST) standard. See *https://host-oa.com/*

The standard provides a Modular Open Systems Approach (MOSA) as an Open Systems Architecture (OSA) definition of virtual and physical interfaces to hardware components such that interoperability and reuse of hardware components can be realized. HOST intends to establish performance and interface requirements that are consistent, open, enforceable, and testable. But, even with the HOST standard, there are many opportunities to improve and achieve the desired objectives of interoperability and accelerated development.

Current methods for designing and integrating hardware systems often involve the use of homegrown and manually maintained knowledge sets captured in home-grown tools by individual engineers and teams. These methods (spreadsheets, docs, databases, etc.) may be suitable for specific and utilitarian needs; that is, they get the immediate job of adding up thermal and electrical loads, bus bandwidth requirements, et al. They are likely done correctly and efficiently but with limited value for long term maintenance or collateral application. Typically, these tools are limited by the awareness of the engineering teams and are manually populated with published vendor data by the constrained knowledge base of the engineers involved in the selection process. Given the complexity of options, engineers will often settle with the first solution that "works" or "fits" the need.

Often, these "homegrown" knowledge sets are duplicated and diverge amongst teams, thus creating a data reliability issue. After a project moves through the lifecycle of Research and Development (R&D) and testing, these knowledge sets are no longer needed or maintained.

By the time a deployed system is in sustainment mode, the knowledge set is no longer reliable and requires non-trivial effort to update. New boards will have come to market, which may or may not be useful as replacements. Certain hardware vendors may release newer versions of a board with similar model numbers and abilities but differing electrical or thermal requirements.

2 THE OPPORTUNITY

To conquer these challenges and make both the knowledge sharing and the integration of avionics components easier to use and more cost-effective, the principles of Digital Engineering (DE) must be more thoroughly enabled, developed, and ultimately adopted. DE is an integrated approach that uses digital technologies to transform engineering practices and processes throughout the entire lifecycle of a system.

- DE on the front end helps with the identification, modeling, and design of systems.
- DE during development and production enables a living reference model and data set to adjudicate questions and access design decisions and constraints.
- DE during delivery and sustainment supports maintenance, spares, and logistics.

During the design and engineering phases, the robust use of DE leverages digital twins, simulation, data analytics, and model-based methodologies to enhance the efficiency, quality, and agility of engineering projects. Put another way, instead of relying on document-centric tools

and processes like manuals, specification sheets, and application guides, the community needs to define a consistent structure for representing and capturing a component's physical attributes and performance characteristics to enable robust DE modeling and analysis as part of the engineering process.

Similar DE principles are used in many industries. One such example is the architectural industry. DE has significantly transformed that industry by streamlining and enhancing the entire design process through the usage of advanced digital tools and methodologies. With technologies such as Building Information Modeling (BIM) and digital twins, designers can create highly detailed and accurate models that improve visualization, collaboration, and decision-making.

These tools allow for real-time simulations and analyses, enabling early detection of design flaws and optimization of structural integrity and energy efficiency. Furthermore, digital engineering facilitates seamless integration and coordination among various stakeholders, reducing errors, accelerating project timelines, and lowering costs. The adoption of cloud-based platforms ensures that all design data is accessible and up to date to the broadest possible community, fostering a collaborative environment and enhancing productivity across the project lifecycle.

The reach of DE is ever-expanding. While there are valuable uses of DE techniques currently at work in the avionics space, our research has shown that these innovative approaches can be applied in the areas of designing and integrating computing systems in the avionics computer thus delivering similar benefits. A digital representation of the capabilities, compatibilities, and constraints of avionics computing components opens the door to a multitude of applications and benefits.

Pulling together a registry of avionics components based on a standardized digital format would allow vendors to expose products to potential customers, likewise, consumers could easily browse and compare offerings from across the market with homogenous data for each product. Vendors and integrators could publish and autogenerate documentation and specifications with the click of a button based on the data stored in the registry. Visualizations could be created for representing and communicating connectivity diagrams and pinout tables. Intelligent searches could be performed to find the "best fit" or "most affordable" options for replacing components that are no longer produced. Finally, tools that model the integration process could be created that would give real-time feedback to users while assembling a build without having to procure any products.

3 RESEARCH RESULTS

One example of innovative research into DE approaches to increase modeling and integration solutions for avionics computer design is the work done by SimVentions for the US Navy Naval Air Systems Command (NAVAIR). As a result of this effort, SimVentions created the HOST Hardware Integration Toolkit (HHITS). HHITS is a set of interoperable cloud-hosted components

that show the value of and opportunities of pulling the DE thread to enable transformative automation, artificial intelligence, and integration for avionics systems.

To address challenges, the following steps were realized:

- Develop a robust but flexible data format to capture the characteristics of avionics systems and the components.
- Create a repository for managing component data.
- Implement visualization methods to represent the hierarchy of components.
- Develop automated decision aids and Artificial Intelligence tools to assist with composition and integration decisions.
- Develop reporting and documentation formats from the standardized data.
- Deploy a robust prototype for community use and exploration.

3.1 ROBUST AND FLEXIBLE DATA FORMAT

Initial development involved defining a format to capture the characteristics of an avionics computing component. A JavaScript Object Notation (JSON) format was chosen because it is easily parsed by computers and is human-readable enough to facilitate understanding and experimentation during the early stages of development. The goal was to capture details down to the pin-out level so that tools could analyze these definitions and detect incompatibilities during integration. An example of the format for a module is shown in Figure 3-1: Component library for discovery and composition.

"na	me":	"SLT3-PAY-2	F2U-14.2	.3",							
"wi	dth":	"96",									
"la	yout'	: "slotprof	ile",								
"ch	ildre	en": [
	{										
		"name": "ke	y1",								
		"label": ""									
		"type": "ke	Υ",								
		"x": "0",									
		"y": "0",									
		"height": "	1",								
		"width": "1	2"								
	1.										
	{										
		"name": "j0	",								
		"label": "S	E\nJ0",								
		"type": "ut	ilitypla	ne",							
		"x": "0",									
		"y": "1",									
		"height": "	8",								
		"width": "1	2",								
		"columns":	["", "Row	I", "Row H",	"Row G", "Row	F", "Row	E", "Row D",	"Row C", "Row	B", "Row A"],	,	
		"signals":	[
		"1",	"Vsl",	"Vsl",	"Vsl",	"Vsl",	"No_Pad*",	"Vs2",	"Vs2",	"Vs2",	"Vs2",
		"2",	"Vsl",	"Vsl",	"Vsl",	"Vsl",	"No_Pad*",	"Vs2",	"Vs2",	"Vs2",	"Vs2",
		"3",	"Vs3",	"Vs3",	"Vs3",	"Vs3",	"No_Pad*",	"Vs3",	"Vs3",	"Vs3",	"Vs3",
		"4",	"GND",	"SM2",	"SM3",	"GND",	"-12V_Aux",	"GND",	"SYSRESET*"	"NVMRO",	"GND",
		"5",	"GND",	"GAP*",	"GA4*",	"GND",	"3.3V_Aux",	"GND",	"SM0",	"SM1",	"GND",
		"6",	"GND",	"GA3*",	"GA2*",	"GND",	"+12V_Aux",	"GND",	"GA1*",	"GA0*",	"GND",
		"7",	"TCK",	"GND",	"GND",	"TDO",	"TDI",	"GND",	"GND",	"TMS",	"TRST*",
		"8",	"GND",	"REF_CLK-",	"REF_CLK+",	"GND",	"GND",	"AUX_CLK-",	"AUX_CLK+",	"GND",	"GND"
		-									

Figure 3-1: Component library for discovery and composition.

Once the data format was established, a repository was built to allow for entering, browsing, searching, and updating component data. Various technologies were evaluated for the repository, and MongoDB was chosen. MongoDB, a NoSQL database, is known for its flexibility and scalability, storing data in JSON-like documents with dynamic schemas. It is designed to handle large volumes of unstructured data and supports a wide range of applications, from simple web apps to complex, data-intensive operations. See Figure 3-2.

ŧ	Builds	Repository Rules	The data in this application is for testing purposes a	nd may contain modified or unrealistic data.		0 ¢ 1
					3U P.	AY 16.2.3-11 X
C	Chassis Q Sea	Backplanes Modules Mezzanines Power Suppl	ies	12 6 17 10 m Compare + New :	Name Vendor	3U PAY 16.2.3-11
					Туре	Payload
		Name 个	Туре	Module Profile	Form	3U
	_				Protessors	MOD3-PAY-2F2U-16.2.3-11
		234-0C0-SBC	Payload	SIMV30_PAY_ABC, M003+PAY-1F1F20110111011-16.2.15-4	Туре	AMD
					Speed	2 ghz
		3U PAY 16.2.3-11	Payload	MOD3-PAY-2F2U-16.2.3-11	Memory	64 mb
					Total Power Data Planes	40 watts
		3U PAY 16.2.3-3	Payload	MOD3-PAY-2F2U-16.2.3-3	Pipe Size	FP
					Protocol	PCIe
		3U PAY 16.2.3-3 Tram's Version	Payload	M0D3-PAY-2F2U-16.2.3-5	Version	Gen 3
					Quantity	2
		3U PAY 16.2.3-5	Payload	MOD3-PAY-2F2U-16.2.3-5	Control Planes	
					Pipe Size	UTP
	_				Protocol	PCle
		3U PAY 16.2.3-5 (Duel Profiles)	Payload	MOD3-PAY-2F2U-16.2.3-5, MOD3p-PAY-1F1U1S1S1U1U2F1H-16.6.11-4	Version	Gen 2
					Width	
		3U PAY 16.4.15-1	Switch	MOD3-SWH-6F1U7U-16.4.15-1	Quantity	2
					Planes	
		3U Pay 14.6.11	Payload	M0D3-PAY-1F1U1S1S1U1U2F1H-16.6.11-1, M0D3p-PAY-1F1U1S1S1U1U2F1H-16.6.11-1	Mezzanine Count	0
					Ruggedization	Level 5
				Rowsperpage: 10 ♥ 1-10 07 63 < < >>1	Owner	Art Vandelay
					World	ReadWrite

Figure 3-2: HHITS repository.²

3.2 INNOVATIVE VISUALIZATION

With a repository of components established, innovative visualization methods have been developed. These include a tree view to represent the hierarchy of components in a build and a virtual backplane with slots for drag-and-drop module placement. Additionally, dynamic and interactive rendering of module and slot profiles is now possible based on data captured in the newly created format (see Figure 3-3). Together, these advancements create a virtual workbench for integrating avionics computing components.

² https://rockwellcollinsthoughtleadership.wordpress.com/wp-content/uploads/2018/06/avionics-opensystem-architecture-standardization-shepherd-and-wills.pdf



Figure 3-3: HHITS interactive diagram.³

3.3 DEMONSTRATE AUTOMATED AND AI INTEGRATION TOOLS

Finally, with the capability to virtually integrate components and their associated data, comprehensive evaluation of the entire build became possible. Key metrics such as total required power and overall weight can now be calculated. Additionally, build requirements were digitally captured as rules.

For example, requirements like having at least two USB ports or 256 gigabytes of memory are encoded as rules and evaluated against the build using a rule engine. These rules can be created and edited as necessary and are organized into rule sets that can be easily turned on or off during the build process. Figure 3-4 shows a screenshot of the HHITS workbench.

³ https://aws.amazon.com/blogs/industries/understanding-digital-engineering-and-how-it-drivesbusiness-value/

Builds > Tram Test	Rep	ository	Rules	The data in this application	on is for testing purposes and may contain modified or unrealistic data.						0	¢ 1
E Build Tree 🖬 Summary	^ < E	Back to 61	U Test Cha	issis		8 Messages	🗃 Save Build	E Export	Build	🖬 Libr	ary 🥔 Rules	O Properties
Tram Test ~ 6U Test Chassis ~ BKP6-CEN16-11.2.2-3 ~ CPAY5 * • 3U PAY 16.2.3-3		BKF Data Ethe	P6-CEN16- a Plane pro armet 10GE	N16-11.2.2-3 ./122-3 stocol mismatch between modules - (Slot 1 ↔ Slot 8 FP Ethernet 10G JASE-KX4) (Slot 1 ↔ Slot 2 FP PCIe Gen 2).	BASE-KX4) (Slot 2 FP PCIe Gen 2 ↔ Slot 8) (Slot 2 ↔ Slot 8 FP		Ø	y ,		Q [50:	Modules -	Advanced
 Payload Stot Payload Stot Payload Stot Payload Stot Payload Stot Payload Stot 			TYPE	NAME	SLOT				Î	****	CPAY5 MOD3-PAY-1U-16.3.3-2 Vendor: Company C Mezzanine Count: 1	
3USwitch-14.8.7 Switch Stot Payload Stot Payload Stot		1	?	CPAYS MOD3-PAY-1U-16.3.3-2 This module does not contain 2 USB ports clone and 2 more					Ľ	21112	3U PAY 16.2.3-3	
Payload Slot Payload Slot Payload Slot Payload Slot Payload Slot Payload Slot		2		3U PAY 16.2.3-3 MOD3-PAY-2F2U-16.2.3-3 The module does not contain DDM memory. original	SLT6-PAY-4F1Q2U2T-10.2.1			•••			MOD3-PAY-2F20-16.2.3-3 Power: 46.06 Watts Mezzanine Count: 0	
Payload Slot		3	No.	Add a Component	SLT6-PAY-4F1Q2U2T-10.2.1			•••	L		CPAY6 MOD3-PAY-2F2U-16.2.3-3 Vendor: Company C Mezzanine Count: 1	
		4	2112 Terreter	Add a Component	SLT6-PAY-4F1Q2U2T-10.2.1			•••		_	CPAY3	
		5	No.	Add a Component	SLT6-PAY-4F1Q2U2T-10.2.1			•••	4		MOD3-PAY-1U-16.3.3-3 Vendor: Company C Mezzanine Count: 1	
		6	ALL R	Add a Component	SLT6-PAY-4F1Q2U2T-10.2.1			•••		-	CPAY2 MOD3-PAY-1U-16.3.3-2	
		7	ALL R	Add a Component	SLT6-PAY-4F1Q2U2T-10.2.1			•••			Vendor: Company C Mezzanine Count: 0	
		8	?	3USwitch-14.8.7 MOD3-SWH-4F1U7U1J-16.8.7-1 This module does not contain 2 USB ports clone and 2 more	SLT6-SWH-20U19F-10.4.1			•••			CPAY8 MOD3-PAY-1U-16.3.3-3 Vendor: Company C Mezzanine Count: 0	
	Ŧ	9	2\$	Add a Component	SLT6-SWH-20U19F-10.4.1			•••	÷	-	DSWH1	

Figure 3-4: HHITS workbench.⁴

To summarize, this preliminary work has resulted in a prototype known as HOST - Hardware Integration Tool Set (HHITS), which was described in the previous section. HHITS is a web-based application that provides the following capabilities.

- **Repository of Hardware Components**: HHITS allows integrators to search and compare hardware components. This facilitates the selection of suitable components for specific systems, ensuring compatibility and reducing the likelihood of integration issues.
- Interactive Diagrams: HHITS renders the profiles for backplane slots and modules, allowing users to visualize the input/output connections, pin outs and backplane module connections.
- Virtual Build and Simulation: Users can virtually construct a chassis and integrate various hardware components (backplanes, modules, mezzanines, and power supplies) within the tool. By applying predefined rule sets, HHITS identifies and mitigates potential incompatibilities before any physical hardware is procured, saving time and resources.

⁴https://www.researchgate.net/publication/315736224_An_Examination_of_Open_System_ Architectures_for_Avionics_Systems_-_An_Update

- **Standards Compliance**: The tool supports standards like HOST, SOSA[™], and OpenVPX[™], ensuring that the integration process adheres to industry best practices and promotes interoperability among different systems and components.
- Enhanced Collaboration: By providing a unified platform for information sharing and hardware integration, HHITS enhances collaboration among engineers and system integrators, leading to more efficient and effective integration processes.

4 **CONCLUSION**

Our research demonstrates the significant value and potential of advancing HOST data standards in conjunction with MOSA EE concepts, standards, and tooling. This advancement enables a multitude of analytic, economic, and engineering benefits across the avionics ecosystem. In particular, the business benefits of detailed data standards and tools like HHITS are extensive and impactful, promoting comprehensive Digital Engineering practices.

Established vendors can efficiently publish their hardware offerings in a consistent and accurate manner, ensuring clarity and reliability in product information. Buyers can easily find options that meet their specific performance requirements, fostering a competitive market that drives innovation and improves product quality.

Engineers benefit significantly by increasing their speed, accuracy, and confidence through the digital assembly of components before physical prototyping, reducing the risk of errors and accelerating the development process. Ultimately, the community and the public benefit from these advancements through better products delivered faster and at a lower cost, enhancing user satisfaction and contributing to technological progress.

Emerging innovations and standards further support these data-centric engineering capabilities. A key aspect of MOSA is an Enabling Environment (EE), which necessitates a combination of technical, business, and operational improvements throughout the enterprise.

Current efforts by OMG and other standards organizations are focused on defining the standards, patterns, and architectures necessary to realize a MOSA EE. This work, along with the development of Federated Digital Mesh architectures, is creating new opportunities for enhanced, faster, and more accessible engineering capabilities and services within avionics and other complex system domains.

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